795 796 797 798 799 800 801 802 803 804 805 806 807 808 809 810	Viterbi decoding Branch metric calculation Majority decision/voter circuit . Error detection for synchronization control . Error/fault detection technique Parity bit Parity generator or checker circuit detail Even and odd parity Parity prediction Plural dimension parity check Storage accessing (e.g., address parity check) Constant-ratio code (m/n) Check character Modulo n residue check character Code constraint monitored Multilevel coding (n>2)
812	Specified digital signal or pulse count
<u>813</u>	Two key-down detector
<u>814</u>	Data timing/clocking
<u>815</u>	Time delay/interval monitored
<u>816</u>	Two-rail logic
<u>817</u>	Noise level
<u>818</u>	Missing-bit/drop-out detection
<u>819</u>	Comparison of data
<u>820</u>	Plural parallel devices of channels
<u>821</u>	Transmission facility
<u>822</u>	Sequential repetition
<u>823</u>	True and complement data
<u>824 </u>	Device output compared to input
	and the control of th

FOREIGN ART COLLECTIONS

FOR000 CLASS-RELATED FOREIGN DOCUMENTS

Any foreign patents or non-patent literature from subclasses that have been reclassified have been transferred directly to FOR Collections listed below. These Collections contain ONLY foreign patents or non-patent literature. The parenthetical references in the Collection titles refer to the abolished subclasses from which these Collections were derived.

MEMORY TESTING (371/21.1) DIGITAL LOGIC TESTING (371/22.1) DIGITAL DATA ERROR CORRECTION (371/30) FOR100 Scan path testing (LSSD) (371/22.3) FOR101 Including test pattern generator (371/27) FOR102 Block code (371/37.1) FOR103 Memory access (371/40.1) FOR104 Convolutional code (371/43) FOR288 ERROR/FAULT ANTICIPATION (371/4)

. Replacement with spare device or system (371/8.1)

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1 4 ("6378125" "6230119" "5838975" USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	/01/20 13:59 /01/20 13:59 /01/20 13:59
"6378124") and switch US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB OOO4	/01/20 13:59
2 1 717/129.ccls. and context with switch USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB 3 120 717/\$.ccls. and context with switch USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	/01/20 13:59
1 717/129.ccls. and context with switch USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US	/01/20 13:59
1 717/129.ccls. and context with switch USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	/01/20 13:59
US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	/01/20 13:59
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US-PGPUB;	
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US-PGPUB;	
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IBM_TDB 0 717/124.ccls. and (multi adj thread\$4 with USPAT; 2004	/01/14 17:20
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DERWENT;	
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1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	/01/14 17:24
processor US-PGPUB;	
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processor and (multi adj thread\$4) US-PGPUB;	
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		(micro adj engin\$4)	EPO; JPO;	
			DERWENT;	
			IBM_TDB	2004/01/14 17:20
-	0		USPAT;	2004/01/14 17:38
	1	(multi adj thread\$4) and (micro adj	US-PGPUB;	
		engin\$4)	EPO; JPO;	
			DERWENT;	
	254	() () () () () () () () () () () () () (IBM_TDB USPAT;	2004/01/16 12:35
-	354	(break adj point) same count\$4	US-PGPUB;	2004/01/16 12:33
			EPO; JPO;	
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l_	4	(break adj point) same count\$4 same alu	USPAT;	2004/01/16 12:35
]	(bleak ad) point, same country same ald	US-PGPUB;	2001,01,10 12:30
			EPO; JPO;	
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			IBM TDB	
_	2	(break adj point) same count\$4 same alu	USPĀT;	2004/01/16 12:37
	_	and library	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	3	(break adj point) same count\$4 same alu	USPĀT;	2004/01/16 13:06
		and (library or record)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	0004/01/16 10:51
-	0		USPAT;	2004/01/16 12:51
		and (library or record) near (context with	US-PGPUB; EPO; JPO;	
	ŀ	switch)	DERWENT;	
			IBM TDB	
1_	0	 (break adj point) near (context with	USPAT;	2004/01/16 12:51
		switch)	US-PGPUB;	2004/01/10 12:31
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			DERWENT;	
			IBM TDB	
-	0	(break adj point) near (context with	USPAT;	2004/01/16 12:52
		switch\$4)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
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			EPO; JPO;	
			DERWENT; IBM TDB	
_	0	(context with switch\$4) near (break adj	USPAT;	2004/01/16 12:52
-		(context with switch\$4) hear (break adj	US-PGPUB;	2004/01/10 12.02
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
-	1	(break adj point) same count\$4 same alu	USPĀT;	2004/01/16 13:11
		and (library or record) and processor same	US-PGPUB;	
		(multiple or many) with instruction\$4	EPO; JPO;	
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			IBM_TDB	
-	15	("6378125" "6230119" "5838975"	USPAT;	2004/01/16 14:52
		"6378124") and processor same (multiple	US-PGPUB;	
		with instruction\$4 or program\$4)	EPO; JPO;	
			DERWENT;	
	_	/#6270105# #6020110# #6020075#	IBM_TDB	2004/01/20 12:50
-	6		USPAT;	2004/01/20 13:56
		"6378124") and processor same (multiple with instruction\$4 or program\$4) and	US-PGPUB; EPO; JPO;	
		register	DERWENT;	
		1 20920001	IBM TDB	
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